Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	7	"764406".ap.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/12 16:15
S2	93	newman-pau\$.in.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 08:56
S 3	25	newman-paul.in.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 08:53
S4	4169	713/400.ccls. 713/401.ccls. 713/500.ccls. 713/501.ccls. 713/502.ccls. 713/503.ccls. 713/600.ccls. 713/601.ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 09:25
S5	1007817	driver\$1 pre\$driver\$1 buffer\$1 inverter\$1	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 13:27
S6	509636	core	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 13:27
S7	7035	processor near2 core	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 13:42
S8	2766	voltage near5 domain\$1	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 13:28
59	1356	S5 same S7	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 13:30
S10	722	S5 with S7	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 13:30
S11	2	S8 same S10	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 13:32

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\$12 ·	18550 : :	"3.3" same "5.0"	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 13:32
S13	543	S5 same S12	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 13:33
S14	28	S5 same S6 same S12	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 13:33
S15	3990	voltage near2 core	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 13:42
S16	2187	I/O near2 voltage	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 13:42
S17	60	S15 same S16 same S5	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 14:07
S18	87366	S5 same delay\$3	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 14:09
S19	60	S7 same S18	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 14:20
S20	1	S19 same voltage\$1	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 14:09
S21	126149	voltage near6 convert\$4	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 14:21
S22	22	S15 same S16 same S21	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 14:21
S23	361218	clock\$1	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 14:27

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S24	1007817	driver\$1 pre\$driver\$1 buffer\$1 inverter\$1	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 14:44
S25	3990	core near2 voltage	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 14:30
S26	2187	I/O near2 voltage	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 14:30
S27	361218	clock\$1	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 14:31
S28	650642	ff\$1 flip\$flip\$1 latch\$2 register\$1	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 14:35
S29	4	S24 same S25 same S26 same S28	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 14:35
S30	107349	voltage near3 (translat\$4 convert\$3 conversion\$1)	US-PGPUB; USPAT; EPO; IBM_TDB	OR !	OFF	2006/05/15 14:38
S31	22	S25 same S26 same S30	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 14:38
S32	0	S27 same S31	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 14:38
S33	18105	invert\$3 near4 clock	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 14:39
S34	163	S30 same S33	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 14:39
S35		S25 same S34	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 14:39

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S36	0	S26 same S34	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 14:39
S37	1	I/O same S34	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 14:41
S38	140	S30 same I/O same core	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 14:41
S39	0	S33 same S38	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 14:42
S40	4	S27 same S38	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 14:42
S41	33891	processor near6 (driver\$1 pre\$driver\$1 buffer\$1 inverter\$1)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 14:44
542 ;	74	S41 same I/O same core	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 14:44
S43 	281	S25 same S26	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 15:01
' S44	. 22	S27 same S43	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 15:18
S45	507	enable near10 level near10 (shifter converter translator)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 15:23
S46	7	S45 same I/O same core	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 15:19
S47	2616	enabl\$3 near10 (level voltage) near10 (shifter converter translator)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 15:29

548	2	(S47 same I/O same core) not S46	US-PGPUB;	OR	OFF	2006/05/15 15:24
			USPAT; EPO; IBM_TDB			
S49	138	S33 and S47	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 15:30
S50	79	S49 and delay	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 15:30
S51	9	S33 same S47	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 15:30
; S52	62388	(level voltage) near3 (shifter converter translator)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 16:09
S53	29	S25 same S26 same S52	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 16:09
S54	705554	latch\$3 ff\$1 flip\$flop\$1 register\$1	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 16:10
S55	3	S53 same S54	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 16:10
S56	1904416	input output I/O	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 16:17
S57	1904416	input output I/O	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 16:56
S58	705554	latch\$3 ff\$1 flip\$flop\$1 register\$1	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 16:28
S59	62388	(level voltage) near3 (shifter converter translator)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 17:27

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S60 	5307	S57 same S58 same S59	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 16:29
S61	3264	(S57 near6 S58) same S59	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 16:29
S62	3990	core near2 voltage	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 16:32
S63	2187	I/O near2 voltage	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 16:32
S64	10	S61 same S62	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 16:32
S65	1916	326/80.ccls. 326/81.ccls. 327/333. ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 17:09
S66	1641841	output I/O	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 16:56
S67	165934	S66 near6 S58	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 16:57
S68	432	S65 and S67	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 16:58
S69	734	core near3 latch	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 16:58
S70	1182	I/O near3 latch	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 17:05
S71	6	S69 same S70	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 16:58

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S72	2	S71 and S65	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 17:02
S73	47456	(input output I/O) near3 latch	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 17:05
S74	254	S73 and S65	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 17:06
S75	2	S69 and S73 and S65	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 17:06
S76	2177	(326/62.ccls. 326/80.ccls. 326/81. ccls. 327/333.ccls.)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 17:13
S77	10	S70 and S76	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 17:14
S78	8864	I/O near3 S58	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 17:15
S79	24	S76 and S78	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 17:23
580	1	"5,327,317".pn.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 17:20
S81	4169	713/400.ccls. 713/401.ccls. 713/500.ccls. 713/501.ccls. 713/502.ccls. 713/503.ccls. 713/600.ccls. 713/601.ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 17:23
S82	205	S81 and S78	US-PGPUB; USPAT; EPO; IBM_TDB	OR :	OFF	2006/05/15 17:23
S83	22	S59 same S78	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 17:28

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\$84	62388	(level voltage) near3 (shifter converter translator)	US-PGPUB; USPAT; EPO;	OR	OFF	2006/05/15 18:21
			IBM_TDB			
\$85	8864	I/O near3 (latch\$3 ff\$1 flip\$flop\$1 register\$1)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 17:33
S86	4474	core near3 (latch\$3 ff\$1 flip\$flop\$1 register\$1)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 17:32
\$87	2	S84 same S85 same S86	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 17:33
S88	12342	I/O near3 (latch\$3 ff\$1 flip\$flop\$1 register\$1 switch\$2 (transmission adj1 gate))	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 17:33
S89	9308	core near3 (latch\$3 ff\$1 flip\$flop\$1 register\$1 switch\$2 (transmission adj1 gate))	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 17:33
S90	2	S84 same S88 same S89	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 17:33
S91	1007817	driver\$1 pre\$driver\$1 buffer\$1 inverter\$1	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 17:34
S92	3598	S88 same S88 same S91	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 17:35
S93	134	S92 same (shifter converter translator)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 17:35
\$94	2177	(326/62.ccls. 326/80.ccls. 326/81. ccls. 327/333.ccls.)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 17:35
\$95	18	S92 and S94	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 17:48

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S96	149	I/O adj1 latch	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 17:58
S97	13	S84 and S96	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 17:48
S98	166996	(level voltage) near3 (shift\$3 convert\$3 translat\$3)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 17:57
S99	108	core adj1 latch	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 17:58
S10 0	0	S96 same S99 same S98	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 17:58
S10 1	2366	I/O adj1 (latch\$3 ff\$1 flip\$flop\$1 register\$1)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 18:04
S10 2	726	core adj1 (latch\$3 ff\$1 flip\$flop\$1 register\$1)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 18:06
S10 3	0	S98 same S101 same S102	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 17:59
S10 4	18	S98 same S101	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 17:59
S10 5	3	S94 and S101	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 18:03
S10 6	62773	output adj1 (latch\$3 ff\$1 flip\$flop\$1 register\$1)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 18:06
S10 7	13	S102 same S106	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 18:04

S10 8	20628	output adj1 latch\$3	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 18:06
S10 9	195	core adj1 latch\$3	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 18:06
S11 0	1	S108 same S109	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 18:07
S11 1	282	S84 same S108	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 18:09
S11 2	17	S111 and S94	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 18:09
S11 3	3720	I/O adj1 buffer	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 18:17
S11 4	149	I/O adj1 latch	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 18:18
S11 5	2	S113 near10 S114	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 18:20
S11 6	27423	(level voltage) adj1 (shifter converter translator)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 18:21
S11 7	174	I/O adj1 (ff flip\$flop latch)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 18:22
S11 8	0	S116 same S117	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 18:22
S11 9	6	S116 and S117	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/15 18:22

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S12 0	34300	("1.8" same "3.3") ("3.3" same "5. 0")	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/16 07:53
S12 1	4917810	dual two second	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/16 07:54
S12 2	66422	(level voltage) near3 (shifter\$1 converter\$1 translator\$1)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/16 07:54
S12 3	6818	S121 near3 S122	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/16 07:55
S12 4	2180	326/62.ccls. 326/80.ccls. 326/81. ccls. 327/333.ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/16 07:58
S12 5	471	S123 and S124	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/16 07:58
S12 6	304547	latch\$3	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/16 07:58
S12 7	257	S123 same S126	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/16 07:58
S12 8	47 :	S127 and S124	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/16 07:59
S12 9	15098	invert\$3 near3 clock	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/16 08:17
S13 0	46865	(invert\$3 shift\$3 convert\$3 translat\$3) near3 clock	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/16 08:47
\$13 . 1	1341	S122 same S130	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/16 08:18

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S13 2	42	S124 and S131	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/16 08:18
S13	6940	(invert\$3 shift\$3 convert\$3 translat\$3) near6 delay\$3 near6 clock	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/16 08:47
S13 4	102	S122 same S133	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/16 08:49
S13 5	3	S124 and S134	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/16 08:47
S13 6	66422	(level voltage) near3 (shifter\$1 converter\$1 translator\$1)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/16 16:34
S13 7	207	I/O adj1 latch\$3	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/16 16:33
S13 8	16	S136 and S137	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/16 16:33
S13	1972	(level voltage) near3 (shift\$3 convert\$3 translat\$3) near3 clock	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/16 16:34
S14 0	0	S137 same S139	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/16 16:34
S14	0	S136 and S137 and S139	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/16 16:35
S14 2	1	S137 and S139	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/16 16:35
S14 3	2180	326/62.ccls. 326/80.ccls. 326/81. ccls. 327/333.ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/05/16 16:38

S14	62	S139 and S143	US-PGPUB;	OR	OFF	2006/05/16 16:38
4			USPAT;			
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